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## APPLICANTS

Eiichi Murakami, Tokorozawa, JAPAN;

Akio Nishida, Tachikawa, JAPAN;

Kazunori Umeda, Higashimurayama, JAPAN; Kousuke Okuyama, Kawagoe, JAPAN;

Toshiaki Yamanaka, Iruma, JAPAN;

Jiro Yugami, Yokohama, JAPAN;

Shinichiro Kimura, Kunitachi, JAPAN;

## \*\* CONTINUING DATA \*\*\*\*\*

This application is a CON of 10/288,448 11/06/2002 PAT 6,727,146  
 which is a CON of 09/942,668 08/31/2001 ABN *WLL*

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN 2000-316964 10/17/2000 *WLL*

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 06/15/2004

Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 54	TOTAL CLAIMS 5	INDEPENDENT CLAIMS 1
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged <i>WLL</i> Examiner's Signature Initials				

## ADDRESS

REED SMITH LLP  
 Suite 1400  
 3110 Fairview Park Drive  
 Falls Church, VA  
 22042

## TITLE

Semiconductor device and method of manufacturing thereof

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